

IN THE CLAIMS

1 (Currently Amended). A method comprising:
providing a first bus technology on a system;
dividing responsibility for a second bus technology between a controller
integrated on said system and an external component;
determining whether the external component is coupled to said system;
if said external component is not coupled to said system, indicating that said
second bus technology is not available and deactivating said controller; and
when said external component is coupled to said system, indicating that said
second bus technology is available and linking said controller to said external component to
implement said second bus technology.
~~accessing a configuration space on an integrated component that is part of a~~
~~processor based system;~~
~~detecting a component external to said system, said component intended to~~
~~operate with said integrated component;~~
~~comparing an identifier for said external component with an identifier for said~~
~~integrated component; and~~
~~if said identifiers match, writing information into the configuration spaces of the~~
~~integrated and external components.~~

2 (Original). The method of claim 1 including accessing said external component through a bus.

3 (Original). The method of claim 1 wherein accessing a configuration space includes accessing a configuration space on a controller.

4 (Previously Presented). The method of claim 3 including detecting a component external to said system from said controller.

5 (Previously Presented). The method of claim 1 including accessing a configuration space on said component external to said system.

6 (Previously Presented). The method of claim 5 including accessing a global unique identifier from said configuration space on said integrated component.

7 (Previously Presented). The method of claim 6 including accessing a global unique identifier from said configuration space on said component external to said system.

8 (Previously Presented). The method of claim 1 including implementing a capability requiring two functions, one of said functions implemented by said integrated component and the other of said functions implemented by said component external to said system.

9 (Previously Presented). The method of claim 1 wherein writing information includes writing information necessary for the integrated component to communicate with said component external to said system.

10 (Previously Presented). The method of claim 1 including providing a first function through said integrated component and providing a second function through said component external to said system and utilizing said functions to implement a wireless network capability.

11 (Currently Amended). An article comprising a medium storing instructions that enable a processor-based system to:

provide a first bus technology on a system;

divide responsibility for a second bus technology between a controller integrated on said system and an external component;

determine whether the external component is coupled to said system;

if said external component is not coupled to said system, indicate that said second bus technology is not available and deactivating said controller; and

when said external component is coupled to said system, indicate that said second bus technology is available and linking said controller to said external component to implement said second bus technology.

~~access a configuration space on an integrated component that is part of said system;~~

~~detect a component external to said system, said component intended to operate with said integrated component;~~

~~compare an identifier for said external component with an identifier for said integrated component; and~~

~~if said identifiers match, write information into the configuration spaces of the integrated and external components.~~

12 (Original). The article of claim 11 wherein said medium stores instructions that enable a processor-based system to access said external component through a bus.

13 (Original). The article of claim 11 wherein said medium stores instructions that enable a processor-based system to access a configuration space on a controller.

14 (Previously Presented). The article of claim 13 wherein said medium stores instructions that enable a processor-based system to detect a component external to said system from said controller.

15 (Previously Presented). The article of claim 11 wherein said medium stores instructions that enable a processor-based system to access a configuration space on said component external to said system.

16 (Previously Presented). The article claim 15 wherein said medium stores instructions that enable a processor-based system to access a global unique identifier from said configuration space on said integrated component.

17 (Previously Presented). The article of claim 16 wherein said medium stores instructions that enable a processor-based system to access a global unique identifier from said configuration space on said component external to said system.

18 (Previously Presented). The article of claim 11 wherein said medium stores instructions that enable a processor-based system to implement a capability requiring two functions, one of said functions implemented by said integrated component and the other of said functions implemented by said component external to said system.

19 (Previously Presented). The article of claim 11 wherein said medium stores instructions that enable a processor-based system to write information necessary for the integrated component to communicate with said component external to said system.

20 (Previously Presented). The article of claim 11 wherein said medium stores instructions that enable a processor-based system to provide a first function through said integrated component, provide a second function through said component external to said system and utilize said functions to implement a wireless network capability.

21 (Currently Amended). A system comprising:
a processor;
a bus coupled to said processor, said bus capable of using a first and a second bus technology; and
a controller to determine whether an external component to implement said second bus technology is coupled to said system and to indicate that said second bus technology is available when said external component is coupled to said system.
~~a device coupled to said bus, said device including a controller having a configuration space; and~~
~~a mating manager to coordinate the implementation of a capability incorporated in part in said controller and in part in a component external to said system.~~

22 (Currently Amended). The system of claim 21 ~~including a wherein the~~ mating manager ~~to access~~ ~~accesses~~ a configuration space on said controller, ~~detect~~ ~~detects~~ a component external to said system having a configuration space, ~~compare~~ ~~compares~~ an identifier from said external component with an identifier from said configuration space and, if said identifiers match, ~~write~~ ~~writes~~ information into the configuration spaces of said controller and said external component.

23 (Currently Amended). The system of claim 21 wherein said controller ~~device~~ implements a network adapter.

24 (Original). The system of claim 23 wherein said controller implements the medium access control and said component external to said system implements a physical layer.

25 (Currently Amended). The system of claim 22 wherein said external component ~~external to said system~~ is coupled to said system through said bus.

26 (Original). The system of claim 22 wherein said configuration space in said controller includes a global unique identifier and said configuration space on said external component includes a global unique identifier.

27 (Original). The system of claim 26 wherein said mating manager compares said global unique identifiers.